

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 10/007,300
 Confirmation No. 4689
 Filing Date November 8, 2001
 Inventor Keiji Jono et al.
 Assignee KMT Semiconductor, LTD. and Micron Technology, Inc.
 Group Art Unit 2811
 Examiner Thien F. Tran
 Attorney's Docket No. KM1-003
 Customer No. 021567
 Title: Trench-Isolated Transistors, Trench Isolation Structures, Memory Cells,
 and DRAMs

RESPONSE TO SEPTEMBER 25, 2003 FINAL OFFICE ACTION
PRELIMINARY AMENDMENT TO ACCOMPANY RCE FILING

To: Mail Stop RCE
 Commissioner for Patents
 P. O. Box 1450
 Alexandria, VA 22313-1450

VIA EXPRESS MAIL

From: Mark S. Matkin (Tel. 509-624-4276; Fax 509-838-3424)
 Wells St. John P.S.
 601 West First Avenue, Suite 1300
 Spokane, WA 99201-3828

Responsive to the Final Office Action dated September 25, 2003, Applicant
 amends and remarks as follows:

AMENDMENTS

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 TECHNOLOGY CENTER 2800